

## ABOUT ICMASK DESIGN

IC Mask Design is an industry leader in the provision of Physical Design Services to the global semiconductor industry. Our services encompass IC Layout and Training Programs. We deliver expertise in analog, RF, mixed-signal and digital design and provide training courses covering the complete spectrum of Physical Design.



| MASTER-IC TRAINING COURSES |  |
|----------------------------|--|
|                            | Introduction to CMOS Layout                  |
|                            | Analog Layout Techniques                     |
|                            | Analog Layout, Advanced Techniques           |
|                            | RF Layout                                    |
|                            | Very Deep Submicron Layout                   |
|                            | Block Level Layout (Bandgap /PLL /ADC / DAC) |
|                            | Layout for ESD                               |
|                            | High Voltage Layout                          |
|                            | Custom Digital Layout Techniques             |
|                            | Standard Cell Place & Route                  |
|                            | Timing Closure in the Backend Design Flow    |

| 1-DAY SEMINARS  |
|---|
| Layout Parasitics   |
| Layout Techniques for Improved Yield in Silicon   |
| Layout Noise Isolation  |
| High Power MOS Layout   |
| Understanding and Improving Device Parasitics   |
| Developing and Implementing a Layout Flow   |
| See why 500+ engineers across 50+ organizations worldwide have chosen IC Mask Design for their Physical Design training needs   |
| For more information visit <a href="http://www.icmaskdesign.com">www.icmaskdesign.com</a> or email <a href="mailto:training@icmaskdesign.com">training@icmaskdesign.com</a> |



"The IC Mask Design RF layout course provides a structured approach to understanding the challenges involved in RF layout. This is coupled with lots of real world examples and techniques available to produce predictable, repeatable RF layout. This course is now part of our induction program and we have been able to improve our productivity and reduce the amount of RF and analog layout re-work." **Cambridge Silicon Radio**



"The IC Mask Design training courses are a must for anyone looking to take your layout to the next level. The instructors for these courses have been of the highest quality, and truly great teachers." **ON Semiconductor**



"IC Mask Design's training courses have enabled Skyworks Solutions to deliver physical designs of higher quality and reliability than previously experienced. Their training team, with their direct experience, is the key to the entire learning experience. IC Mask Design has provided the additional tools necessary for Skyworks Solutions to be the best solutions provider possible." **Skyworks Solutions**



TRAINING PARTNERS



All courses have received the EuroTraining Quality Standard Label

IC Mask Design delivers expertise in analog, RF, mixed-signal and digital Physical Design



## Sample Projects

## Testimonials

- Family of SERDES devices on 45nm TSMC CMOS
- TV monitor system on 45nm, 32nm & 27nm TSMC CMOS
- WiFi Transmitter Filter on 65nm TSMC DEEPN RF CMOS
- DSP for Multimedia Processing on Mobile Devices on 65nm TSMC
- High speed SERDES 6GHz Flip Chip I/O's on 90nm & 65nm CMOS
- Bluetooth & RF Transceiver for GSM applications on 90nm TSMC DEEPN RF CMOS
- ISM Band Transceiver on 90nm CU (ST/Philips/Motorola)
- Analog frontend for base band processor for mobile applications on 90nm DEEPN CMOS
- Near Field Communications application on 130nm
- Single-chip T-DMB RF receiver and baseband demodulator on 130nm TSMC
- Auto-compensating DC/DC digital controller aimed at Point-of-Load applications on 130nm TSMC



"We had a difficult challenge, designing precision analog circuits in 90nm technology to reside on a baseband processor, in a compressed timescale. IC Mask Design were able to help meet our timescale. They were flexible, and delivered on time within the design constraints imposed. They operated like an extension of our own design team." **MOTOROLA**



"Ceva required a skilled and experienced layout team for a series of complex mixed signal blocks. With a highly accelerated schedule, IC Mask Design responded superbly and were flexible, hard working and communicative, integrating seamlessly into the project team. The next time we have a similar need, IC Mask Design will certainly be our first port of call." **CEVA**