

IC Mask Design to Participate on Panel Discussion at GSA / IET Semiconductor Forum

Chief Technical Officer Ciaran Whyte to present his views on Analog: The New Bottleneck

Limerick, Ireland – April 2010: IC Mask Design is to participate on a panel discussion at the GSA / IET Semiconductor Forum to be held at the Novotel in London from 18-19 May 2010 (<http://www.gsaietsemiconductorforum.com/2010/general/index.asp>).

Also participating in the discussion "Analog: The New Bottleneck" will be Peter Firth, Chief Technical Officer, Wolfson Microelectronics; Doug Pattullo, Director of Field Technical Support, TSMC Europe; and Ed Petrus, Chief Operations Officer, Ciranova.

Despite accounting for only a fraction of overall chip content, analog design is now dominating both critical design path and yield/failure mechanisms. EDA vendors, fabless/IDM organisations and foundries must therefore re-assess design tools and practices to ensure 'right first time' design in a reasonable timeframe.

The panel will explore what can be done at EDA, design and foundry level to improve design knowledge, practice and efficiency.

IC Mask Design has recently launched an innovative Analog Layout Acceleration Platform in partnership with Tanner EDA to help address the issues presented.

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About IC Mask Design

IC Mask Design is an industry leader in the provision of Physical Design Services and Software Solutions to the semiconductor industry. The company's services encompass IC Layout and Training Programs, delivering expertise in Analog, RF, mixed-signal and digital design using the latest tools from leading EDA vendors. IC Mask Design also provides training courses covering the complete spectrum of physical design. Its Analog Layout Acceleration Platform aims to dramatically reduce analog layout design cycle time whilst producing optimal quality layout structures which incorporate circuit functionality requirements and process artefacts.

About GSA

GSA has made a significant impact on the industry since our inception in 1994, playing a vital role with the emergence and worldwide adoption of the fabless business model. Today, we continue our efforts with initiatives and focus to ensure the growth and profitability of the semiconductor industry. GSA's mission is to accelerate the growth and increase the return on invested capital of the global semiconductor industry by fostering a more effective fabless ecosystem through collaboration, integration and innovation.

About IET

The IET is one of the world's leading professional societies for the engineering and technology community, with more than 150,000 members in 127 countries and offices in Europe, North America and Asia-Pacific. The IET provides a global knowledge network to facilitate the exchange of ideas and promote the positive role of science, engineering and technology in the world.

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